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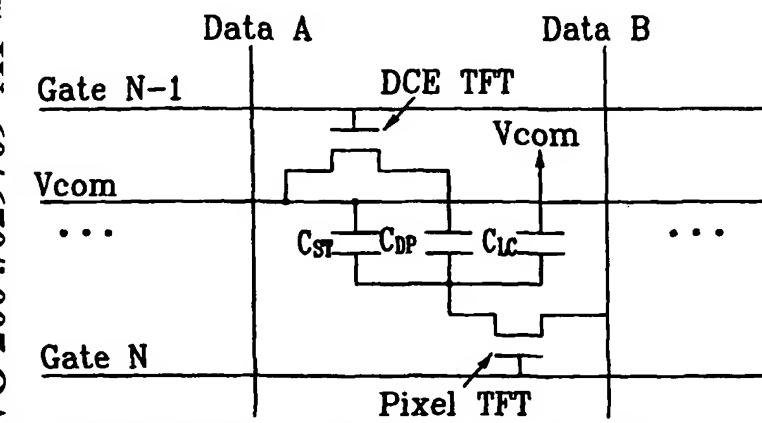
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(54) Title: THIN FILM TRANSISTOR PANEL FOR MULTI-DOMAIN LIQUID CRYSTAL DISPLAY



(57) Abstract: A thin film transistor array panel is provided, which includes: an insulating substrate; a plurality of first signal lines formed on the insulating substrate; a plurality of second signal lines formed on the insulating substrate and intersecting the first wire in an insulating manner; a pixel electrode formed in a pixel area defined by the intersections of the first signal lines and the second signal lines and including a plurality of subareas partitioned by cutouts and a plurality of bridges connecting the subareas; and a direction control electrode formed in the pixel area and including a portion overlapping at least one of the cutouts, wherein two long edges of each subarea are parallel to each other and the at least one of cutouts overlapping the portion of the direction

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